

FIG. 1

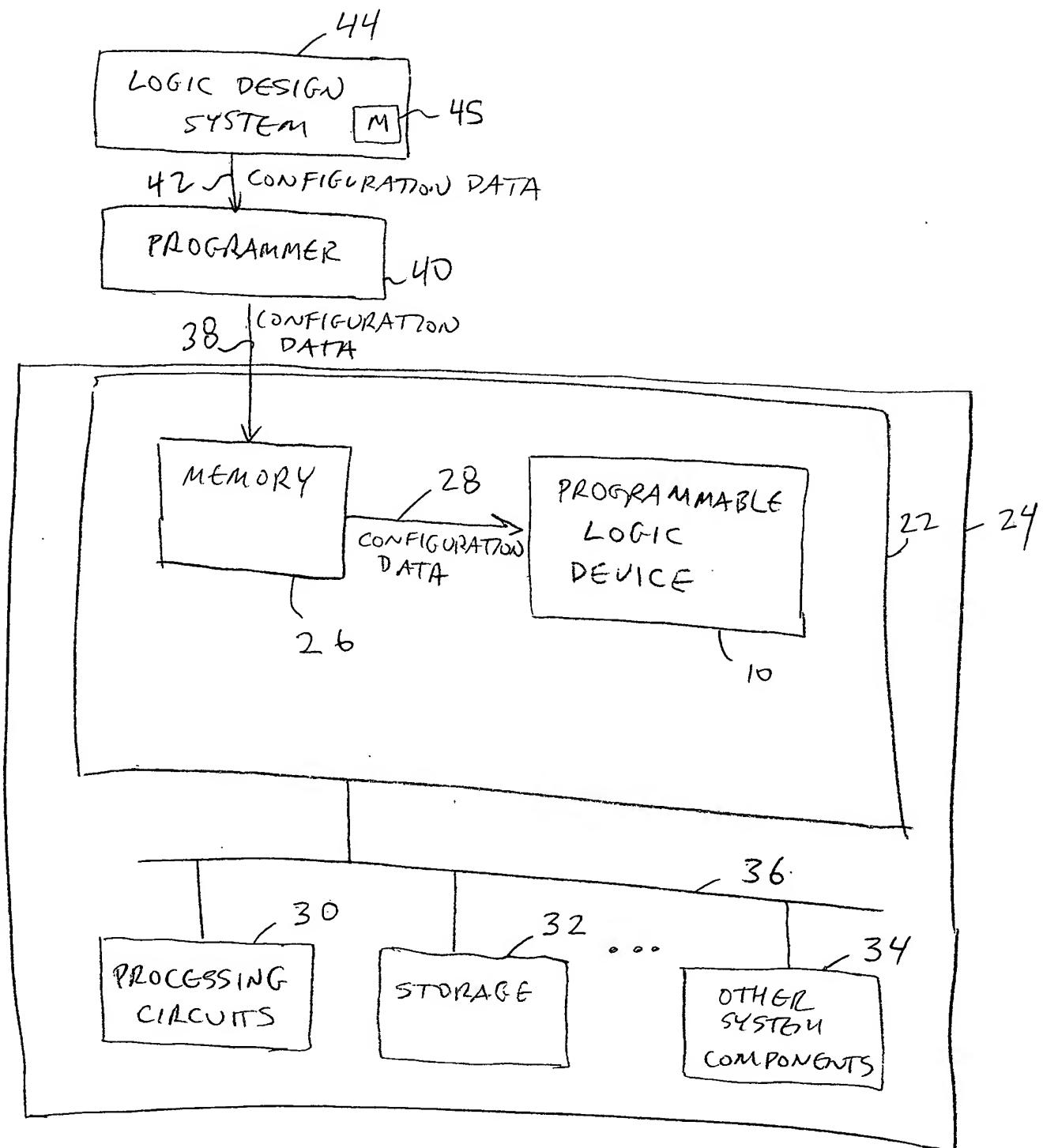


FIG. 2

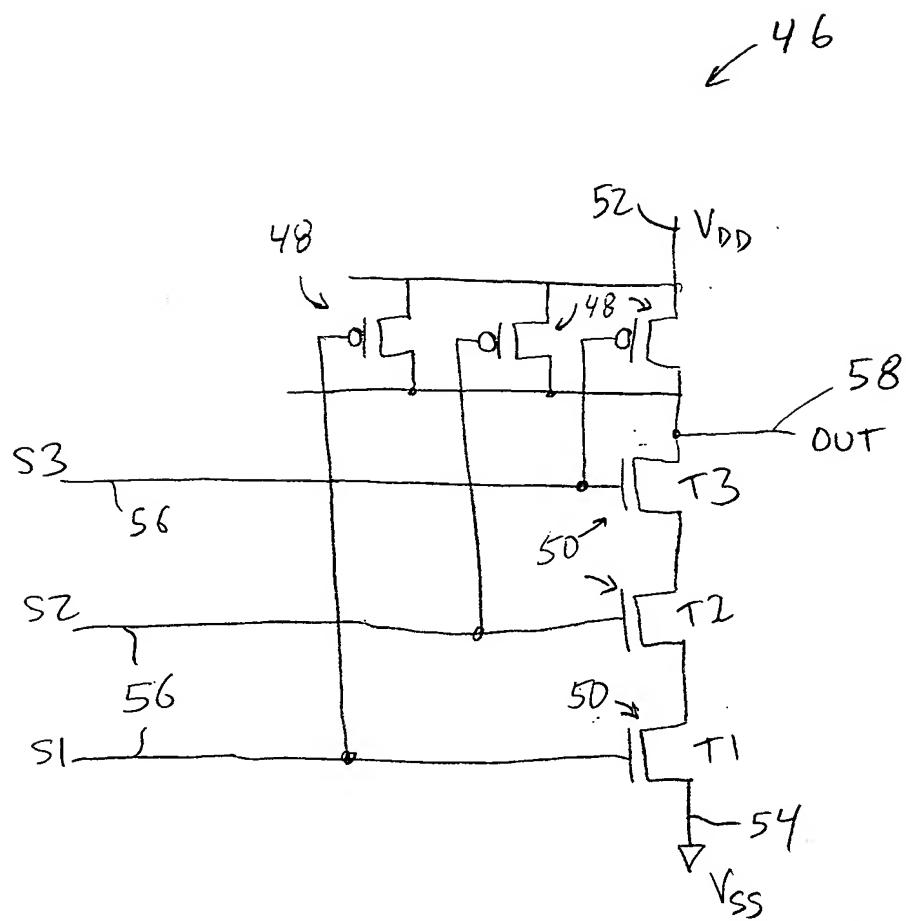


FIG. 3

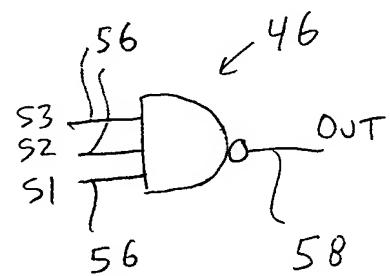
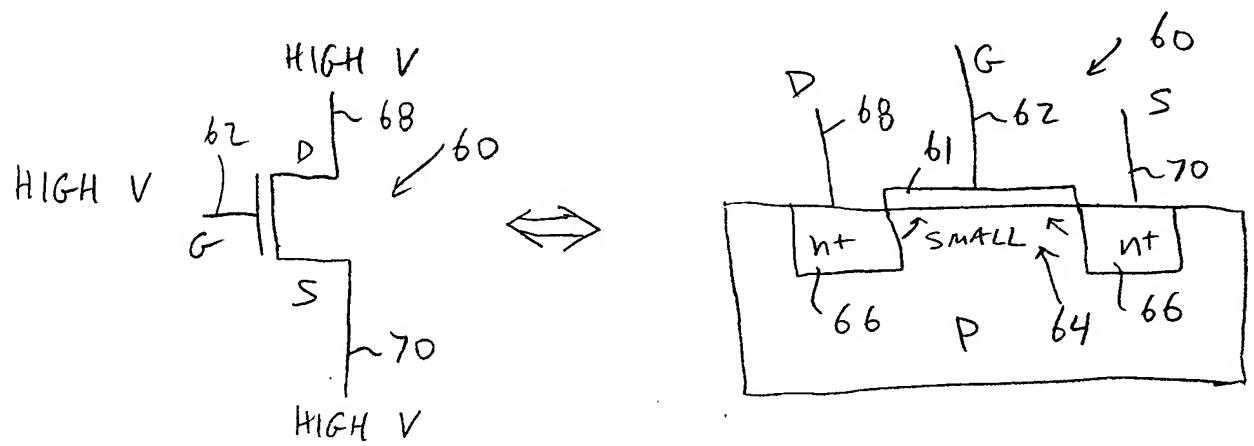
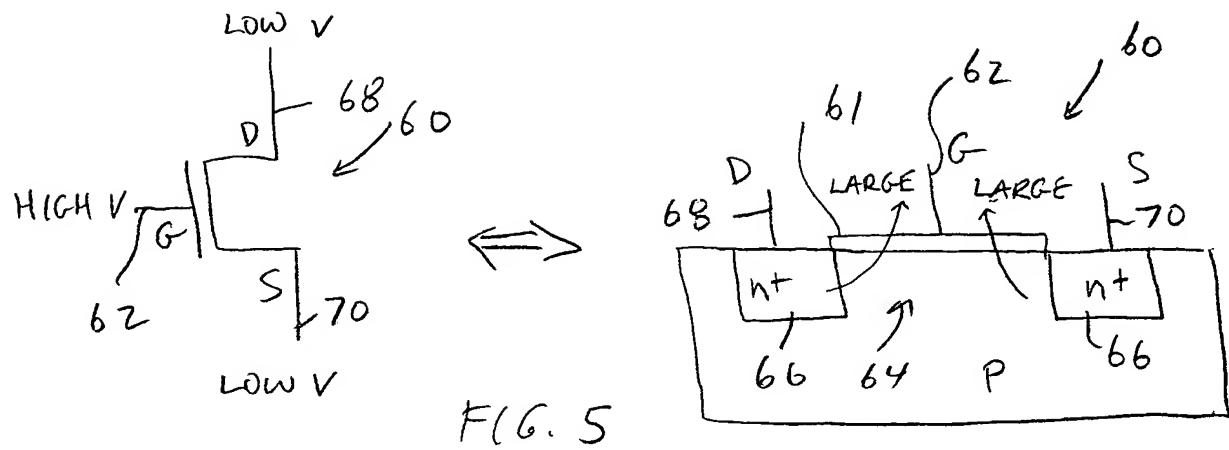


FIG. 4



Row	S1	S2	S3	T1	T2	T3
1	0	0	0	0	0	0
2	0	0	1	0	0	0
3	0	1	0	0	0	LOW
4	0	1	1	0	0	HIGH
5	1	0	0	0	0	LOW
6	1	0	1	0	0	HIGH
7	1	1	0	0	0	HIGH
8	1	1	1	0	0	HIGH

FIG. 7

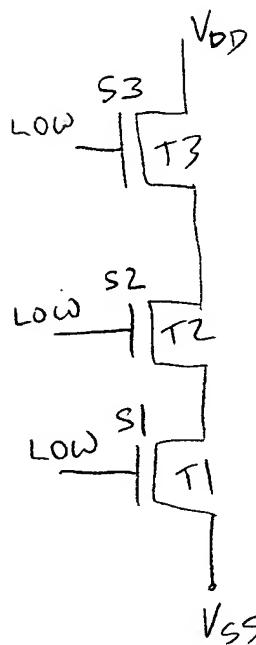


FIG. 9

FIG. 8

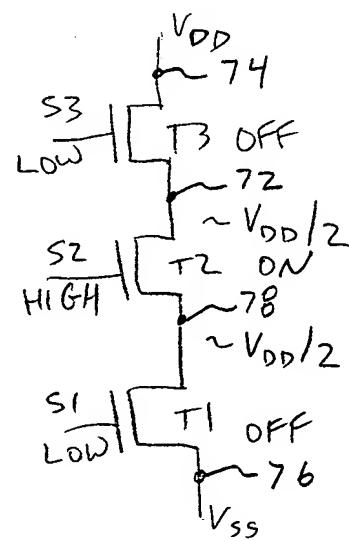
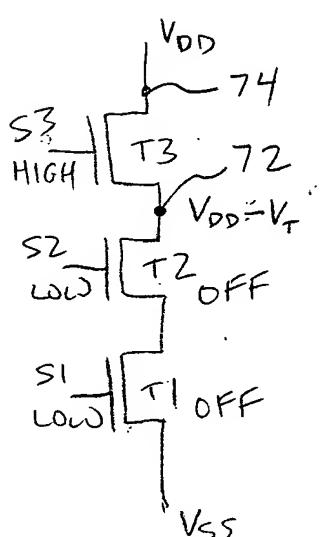
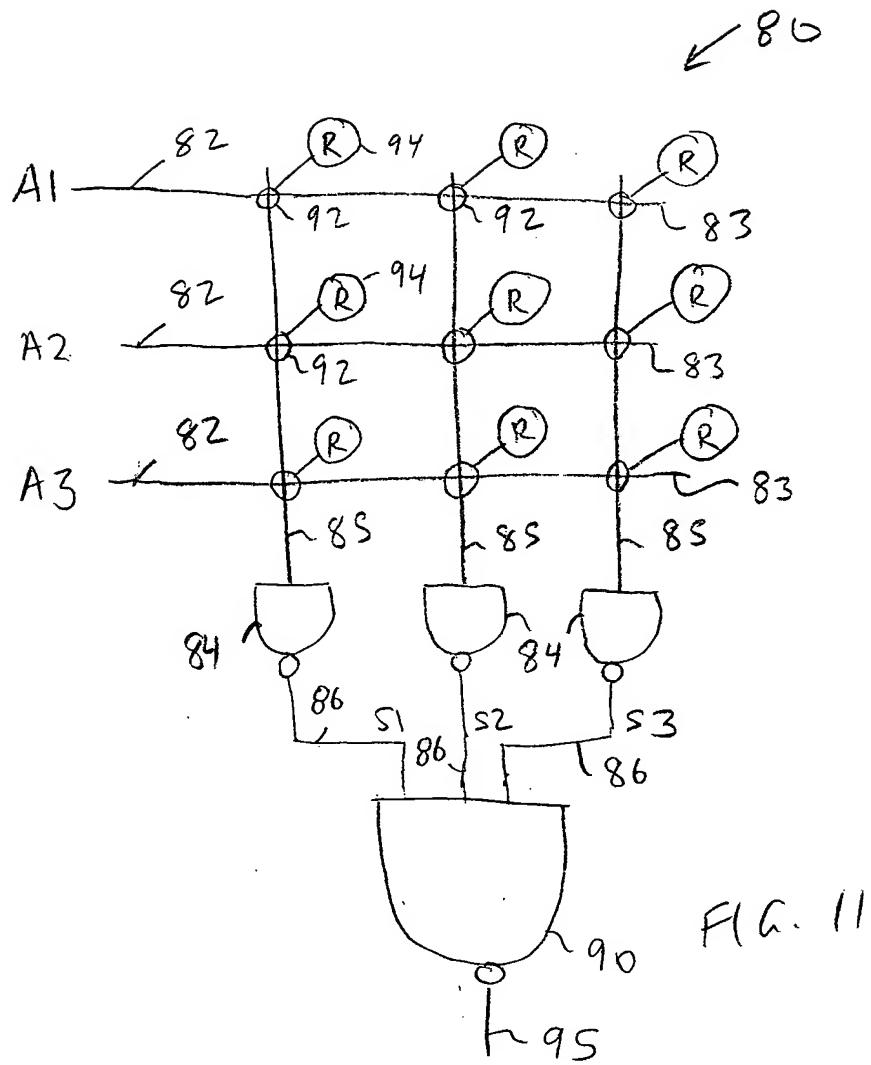


FIG. 10



1	0	0
1	0	0
0	0	0

FIG. 12

0	0	1
0	0	1
0	0	0

FIG. 13

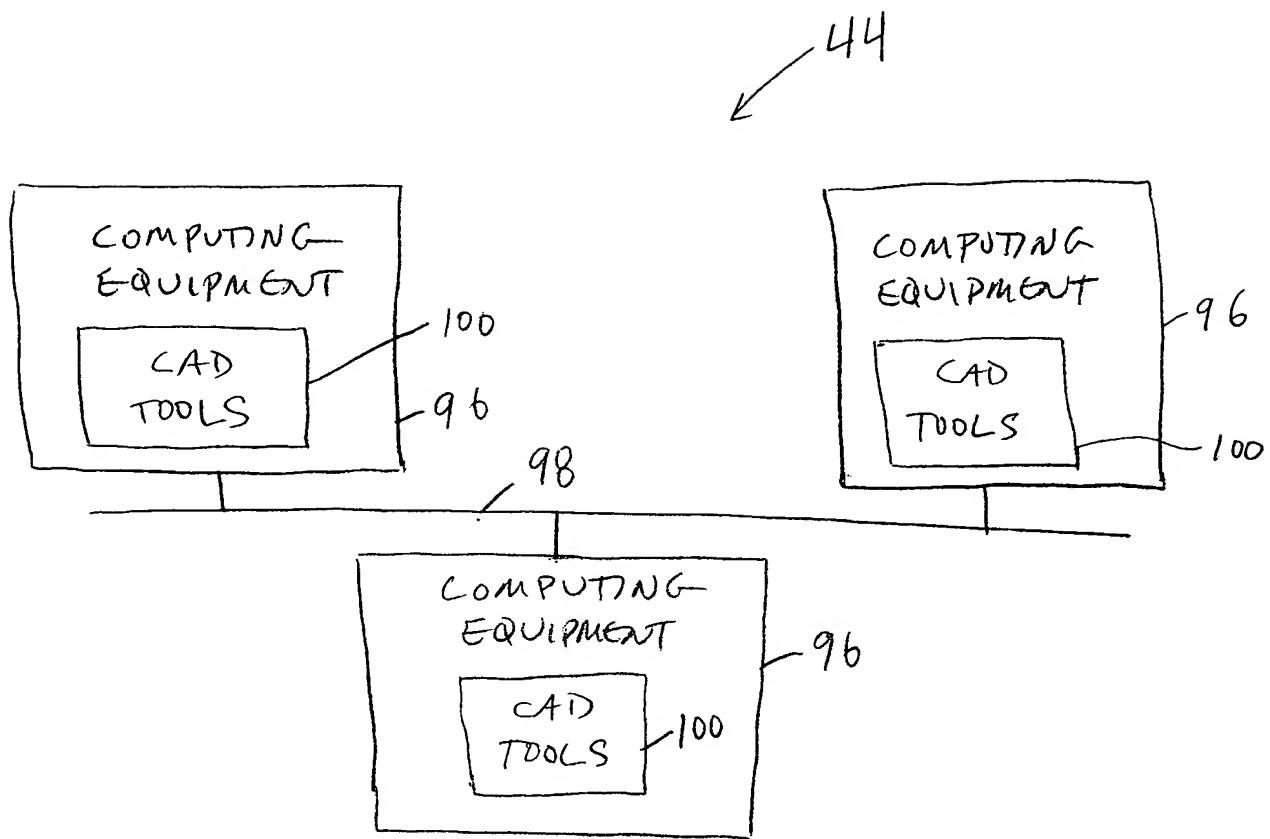


FIG. 14.

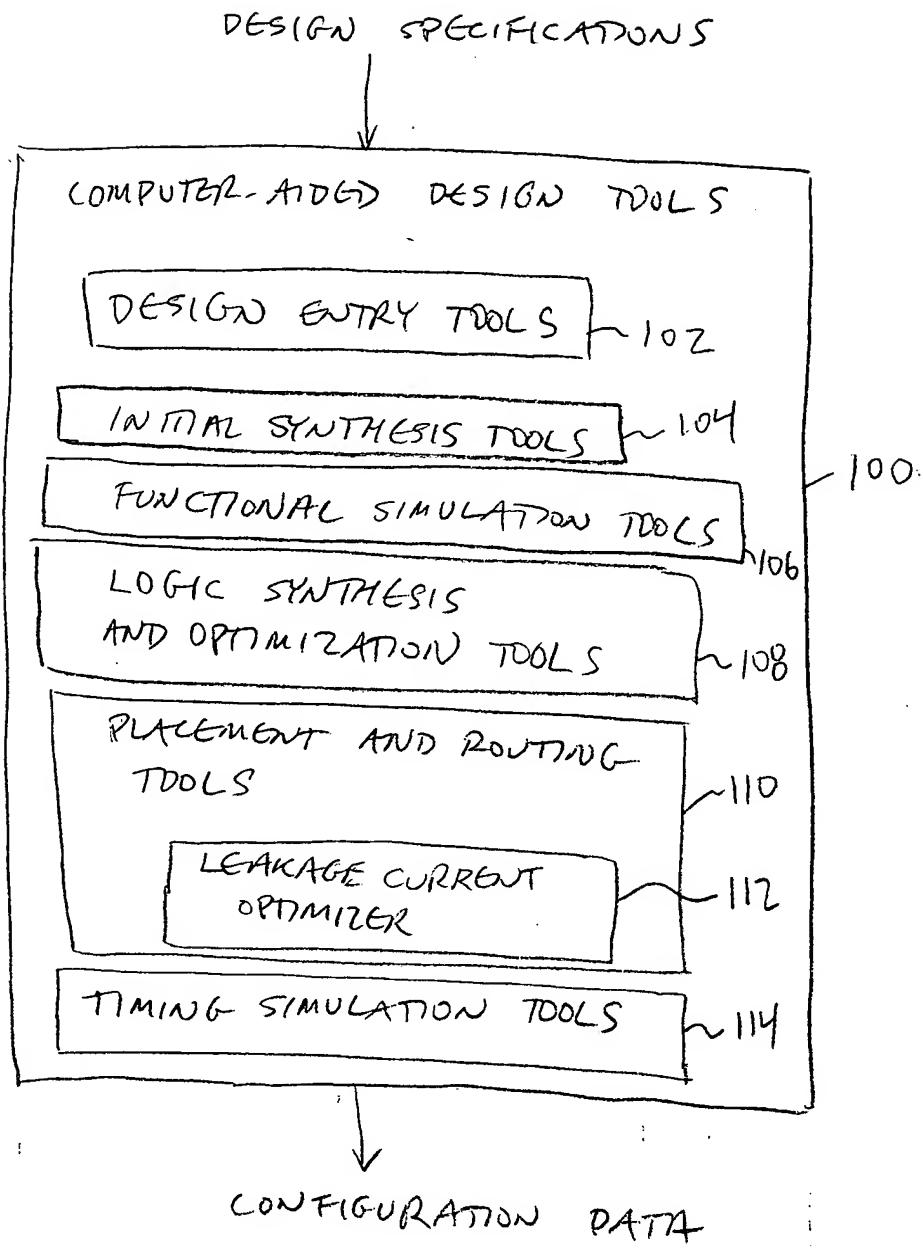


FIG. 15

PERFORM DESIGN ENTRY, INITIAL SYNTHESIS,  
AND FUNCTIONAL SIMULATION.  
GATHER INFORMATION FROM USER  
ON SIGNALS (E.G., INFORMATION  
ON SIGNAL PROBABILITIES, TYPES  
OF SIGNALS, ETC.) AND AUTOMATICALLY  
GENERATE INFORMATION ON SIGNALS  
BY ANALYZING DESIGN. GATHER  
INFORMATION ON DESIGN CONSTRAINTS.

~116

PERFORM LOGIC SYNTHESIS AND OPTIMIZATION,  
PHYSICAL DESIGN, AND TIMING SIMULATION  
(E.G., PERFORM PLACEMENT  
AND ROUTING OPERATIONS, TAKING GATE  
LEAKAGE EFFECTS INTO ACCOUNT TO REDUCE  
POWER CONSUMPTION)  
PRODUCE CONFIGURATION DATA

~118

PROGRAM DEVICE USING  
CONFIGURATION DATA

~120

FIG. 16